Attorney's Docket No.: 07977/204002/US3480D1

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani

Art Unit

2815

Serial No.: 09/389,393 .

Examiner :

Baumeister, B.

Filed

: September 3, 1999.

: SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE Title

Commissioner for Patents Washington, D.C. 20231

1 of Merrical Disclaimer

TRANSMITTAL OF TERMINAL DISCLAIMER

Sir:

This is supplemental to the amendment dated October 4, 2001, and responsive to the Official Action dated July 5, 2001.

The only rejection remaining in the case is the rejection of claim 25 based on judicially created obviousness-type double patenting. Applicants submit herewith a Terminal Disclaimer in order to obviate this rejection.

Since the rejection has been obviated by this submission; it is respectfully suggested that all of these claims should now be allowable. A notice of the merits is respectfully solicited.

The required fee of \$110 for filing a terminal disclaimer is enclosed herewith.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

Signature

Susan Regan

Typed or Printed Name of Person Signing Certificate

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: / 0// 1/0/

Scott C. Harris Reg. No. 32,030

SCH/smr

Fish & Richardson P.C.
PTO Customer No. 20985
4350 La Jolla Village Drive, Suite 500
San Diego, California 92122

Telephone: (858) 678-5070 Facsimile: (858) 678-5099

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
                                                                         Docket No. 07977/204002
        In re PATENT application of
       Hisashi OHTANI
       Serial No.
                     09/389,393
      Filed:
                     September 3, 1999
     For:
                                                                   )
                    SEMICONDUCTOR DEVICE HAVING
                                                                  ) Art Unit: 2815
                   INSULATED GATE ELECTRODE
                                                                 ) Examiner: B. Baumeister
                                                                )
                                TERMINAL DISCLAIMER
Honorable Assistant Commissioner for Patents
Washington, D. C. 20231
                                                            10/22/2001 SHINASS1 00000095 09389393
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Sir: LECHROLOGY CEL I, Dr. Shunpei Yamazaki, having a place of business at Semiconductor Energy Laboratory

Co., Ltd., 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, state that I am authorized sign on behalf of the assignee of this invention and that the Assignment referred to below has been reviewed and certify that, to the best of my knowledge and belief, the entire right, title and interest in the above-identified application is in the name of Semiconductor Energy Laboratory Co., Ltd by virtue of an Assignment recorded in the U.S. Patent and Trademark Office at Reel 8845, Frame

Semiconductor Energy Laboratory Co., Ltd. hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 5,973,378.

Semiconductor Energy Laboratory Co., Ltd. hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and prior Patent No. 5,973,378 are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, Semiconductor Energy Laboratory Co., Ltd. does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 to 156 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 35 CFR 1.321, has all claims cancelled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date

TERMINAL DISCLAIMER
APPROVED

NOV 0 1 2001

TECHNOLOGY CENTER 2800 SPECIAL PROGRAM CENTER

Name: Shunpei Yamazaki

Title: President

Company Name: Semiconductor Energy

Laboratory Co

10/04/200

Supervisory Logal Instrumental Examiner

Technology Center 2800

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